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Filing Date	10/22/2001
First Named Inventor	Efland Taylor, etal
Group Art Unit	2826
Examiner Name	Leonardo Andujar
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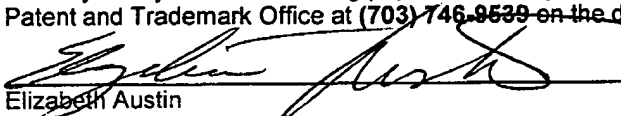
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**In re the Application of: **Efland Taylor, et al.**Docket Number: **TI-30955**Serial No.: **10/039,663**Art Unit: **2826**Filed: **10/22/2001**Examiner: **Andujar, Leonardo**

For: **Individualized Low Parasitic Power Distribution Lines Deposited Over Active Integrated Circuits**

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<b>NAME OF INVENTOR(S):</b> <b>Taylor R. Efland, Milton L. Buschbom, and Sameer Pendharkar</b>	<b>RECEIPT DATE &amp; SERIAL NO.:</b> <b>10/039,663</b>
<b>TITLE OF INVENTION:</b> <b>Individualized Low Parasitic Power Distribution Lines Deposited Over Active Integrated Circuits</b>	<b>FILING DATE:</b> <b>10/22/2001</b>
<b>TI FILE NO.:</b> <b>TI-30955</b> <b>DEPOSIT ACCT. NO.:</b> <b>20-0668</b>	
<b>DATE FAXED:</b> <b>October 7, 2003</b>	
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